

FIG. 1

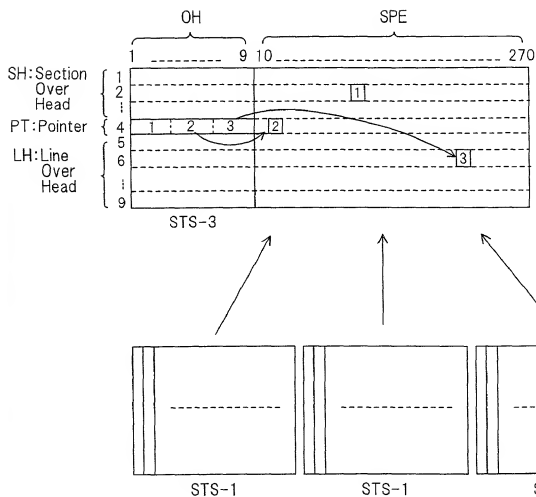


FIG. 2

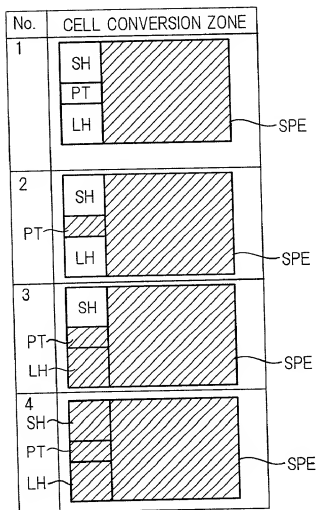


FIG. 3

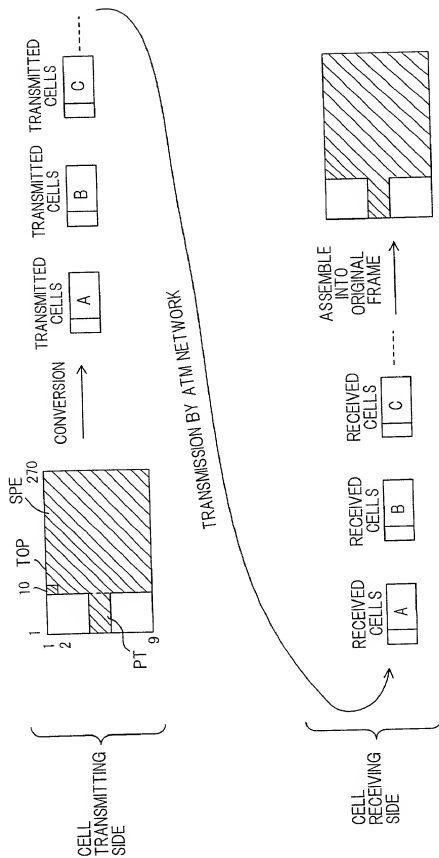
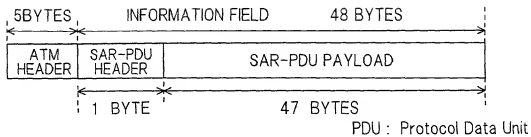


FIG. 4**FIG. 5**

CSI : Convergence Sublayer Identifier
 SC : Sequence Count
 CRC : Cyclic Redundancy Check
 EPB : Even Parity bit

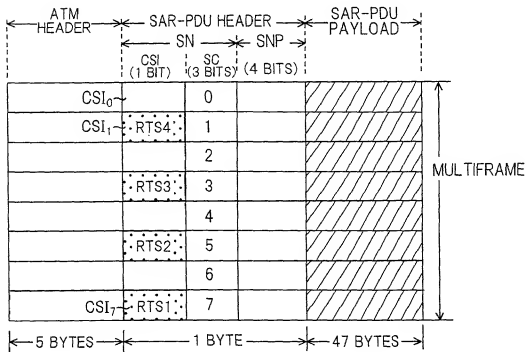
FIG. 6

FIG. 7A

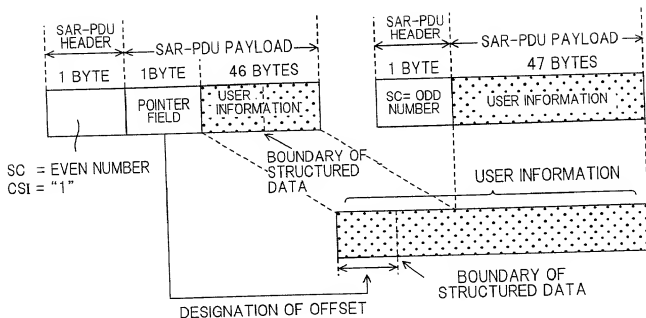


FIG. 7B

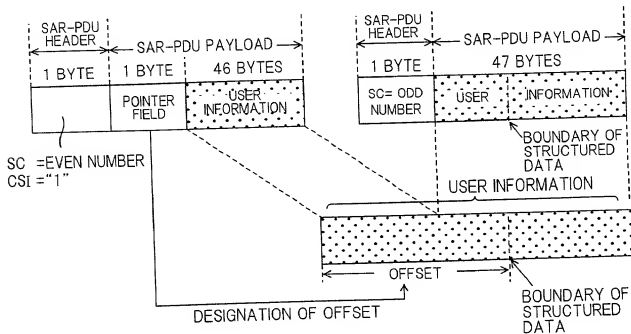


FIG. 8

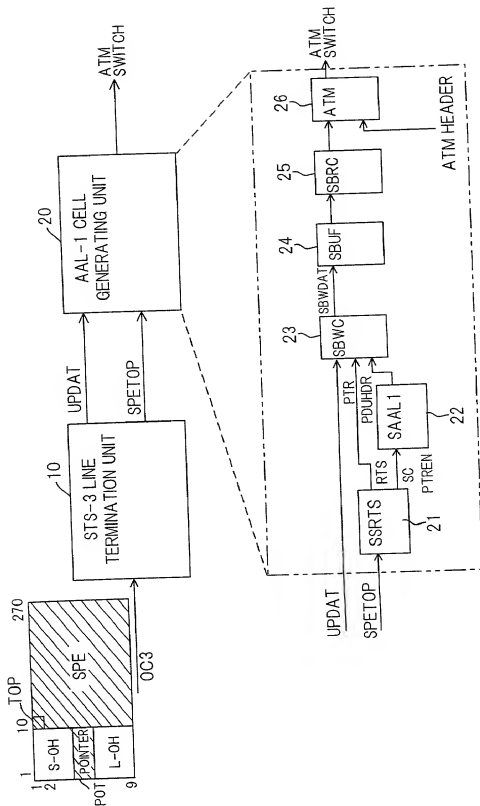


FIG. 9

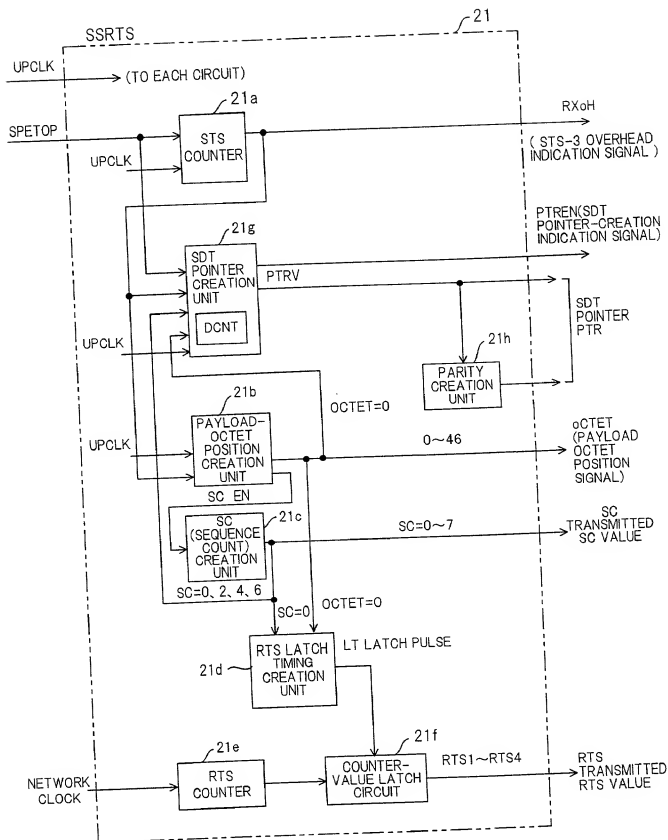


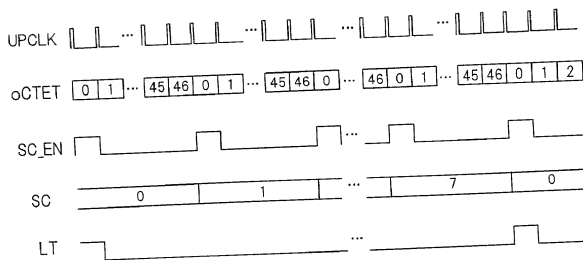
FIG. 10

FIG. 11

POSITION REPORTED BY TOP

A1#1	A1#5	A1#3	A2#1	A2#2	A2#3	J0	Z0#2	Z0#3	SPE 9	SPE 10	SPE 11	SPE 267	SPE 268	SPE 269
0	1	2	3	4	5	6	7	8	9	10	11	267	268	269
B1	270	271	X	272	273	E1	X	274	X	275	276	X	537	538	539
D1	540	541	X	542	543	D2	X	544	X	545	546	X	807	808	809
H1#1	H1#2	H1#3	H2#1	H2#2	H2#3	H3#1	H3#2	H3#3	SPE 819	SPE 820	SPE 821	1077	1078	1079
810	811	812	813	814	815	816	817	818	819	820	821	1077	1078	1079
B2#1	B2#2	B2#3	K1	X	X	K2	X	1087	SPE 1089	SPE 1090	SPE 1091	1347	1348	1349
1080	1081	1082	1083	1084	1085	1086	1087	1088	1089	1090	1091	1347	1348	1349
D4	1350	1351	1352	1353	1354	D5	X	1355	SPE 1359	SPE 1360	SPE 1361	1617	1618	1619
D7	1620	1621	X	1622	1623	D6	X	1357	SPE 1629	SPE 1630	SPE 1631	1617	1618	1619
D10	1890	1891	X	1892	1893	D8	X	1624	SPE 1899	SPE 1900	SPE 1901	1887	1888	1889
S1	Z1#2	Z1#3	M0	Z2#2	Z2#3	D9	X	1627	SPE 2169	SPE 2170	SPE 2171	2157	2158	2159
2160	2161	2162	2163	2164	2165	D12	X	1896	SPE 2169	SPE 2170	SPE 2171	2427	2428	2429
						E2	X	2166						

* LOWER VALUE IS VALUE OF STS-CTR COUNT

* BYTES WITHIN BOLD BORDER UNDERGO AAL-1 CONVERSION (RXoH = "0")

FIG. 12

POSITION REPORTED BY TOP



A1#1	A1#2	A1#3	A2#1	A2#2	A2#3	J0	Z0#2	Z0#3	SPE 2357	SPE 2356	SPE 2355	SPE 2099	SPE 2098	SPE 2097
B1	X	X	E1	X	X	F1	X	X	SPE 2096	SPE 2095	SPE 2094	SPE 1838	SPE 1837	SPE 1836
D1	X	X	D2	X	X	D3	X	X	SPE 1835	SPE 1834	SPE 1833	SPE 1577	SPE 1576	SPE 1575
H1#1	H1#2	H1#3	H2#1	H2#2	H2#3	H3#1	H3#2	H3#3	SPE 1574	SPE 1573	SPE 1572	SPE 1307	SPE 1306	SPE 1305
B2#1	B2#2	B2#3	K1	X	X	K2	X	X	SPE 1304	SPE 1303	SPE 1302	SPE 1046	SPE 1045	SPE 1044
D4	X	X	D5	X	X	D6	X	X	SPE 1043	SPE 1042	SPE 1041	SPE 785	SPE 784	SPE 783
D7	X	X	D8	X	X	D9	X	X	SPE 782	SPE 781	SPE 780	SPE 524	SPE 523	SPE 522
D10	X	X	D11	X	X	D12	X	X	SPE 521	SPE 520	SPE 519	SPE 263	SPE 262	SPE 261
S1	Z1#2	Z1#3	M0	Z2#2	Z2#3	E2	X	X	SPE 260	SPE 259	SPE 258	SPE 2	SPE 1	SPE 0

* LOWER VALUE IS VALUE OF DCNT COUNT

* BYTES WITHIN BOLD BORDER UNDERGO AAL-1 CONVERSION (RXGH = "0")

FIG. 13

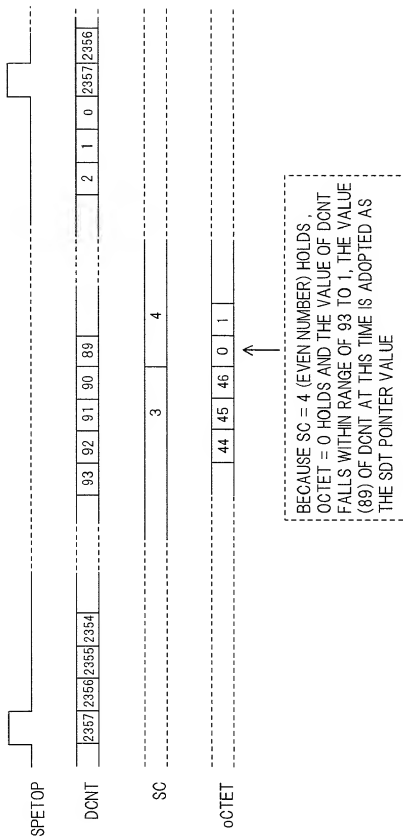


FIG. 14

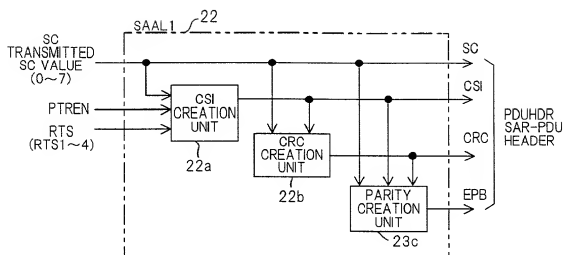


FIG. 15

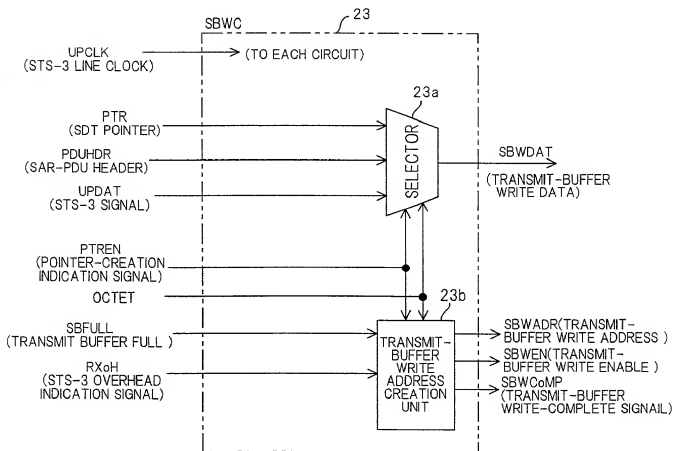


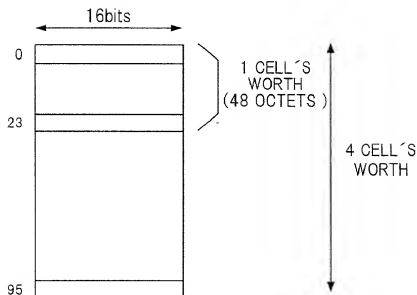
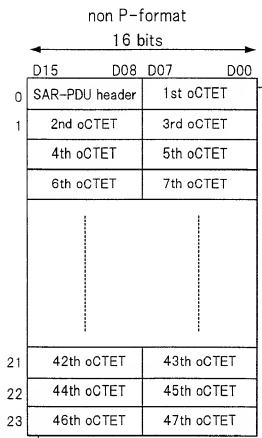
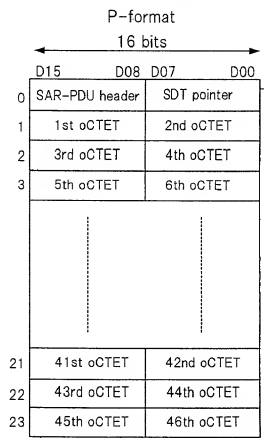
FIG. 16**FIG. 17A****FIG. 17B**

FIG. 18

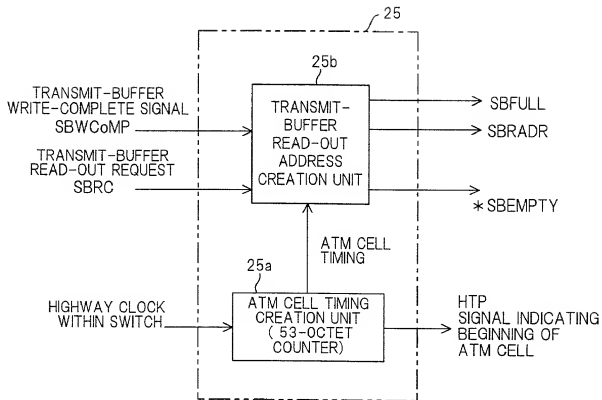


FIG. 19

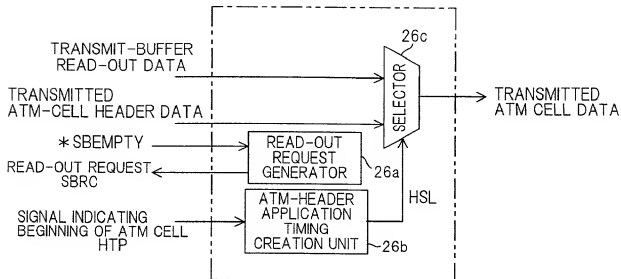


FIG. 20

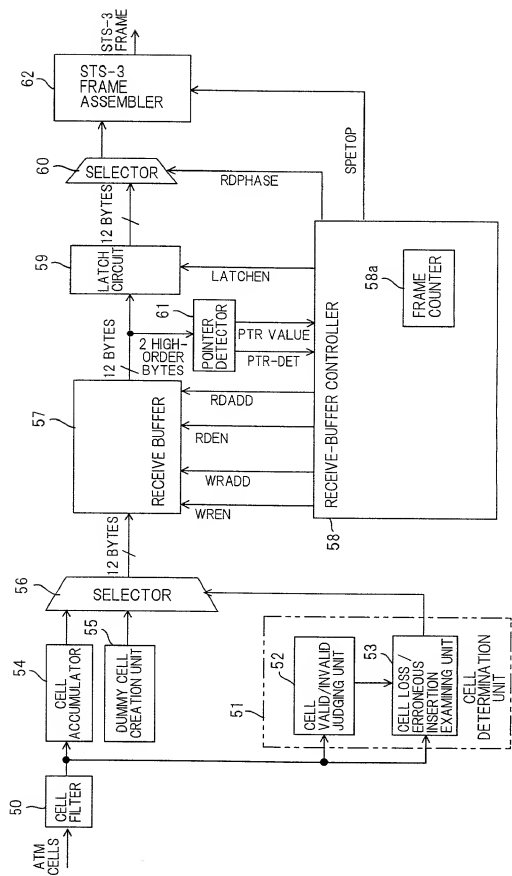


FIG. 22

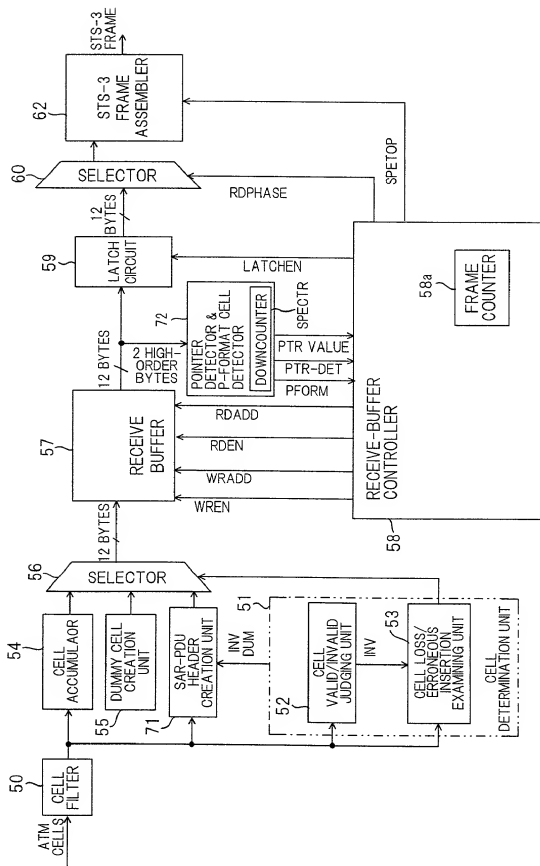
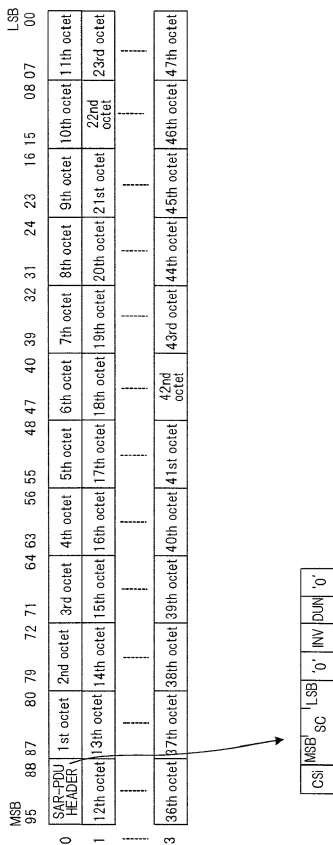


FIG. 23



CS: INDICATES P-FORMAT (0: NON-P-FORMAT, 1: P-FORMAT)
 SC: SEQUENCE COUNT VALUE
 INV: INDICATES HEADER INVALID (0: VALID, 1: INVALID)
 DUM: DUMMY CELL FLAG (0: ORDINARY CELL, 1: DUMMY CELL)

FIG. 24

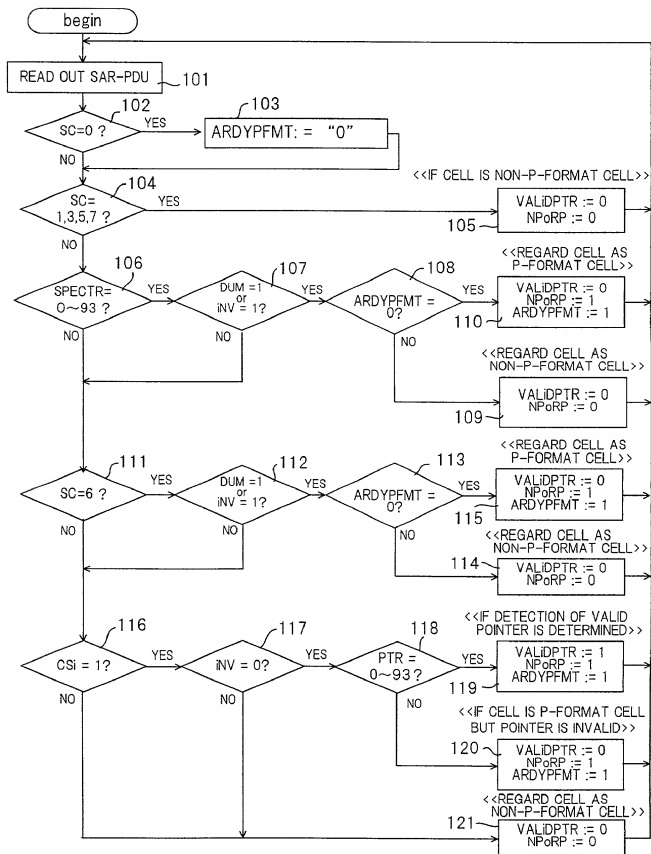


FIG. 25

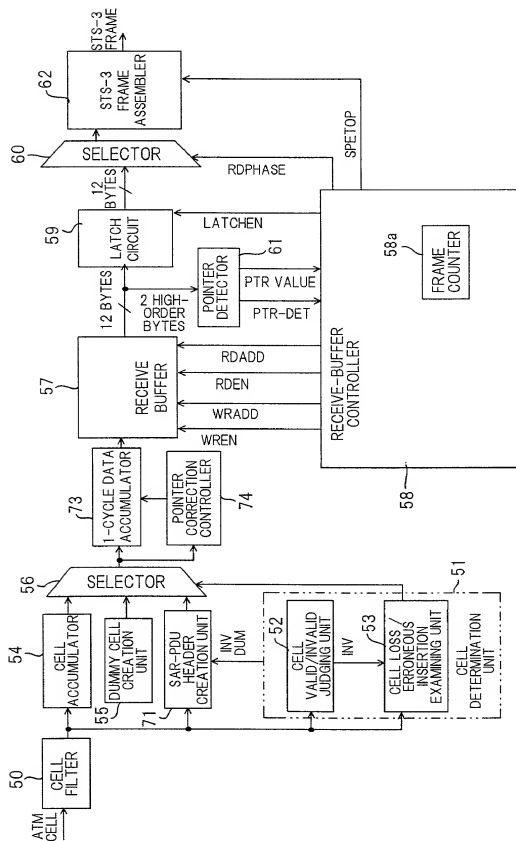


FIG. 26

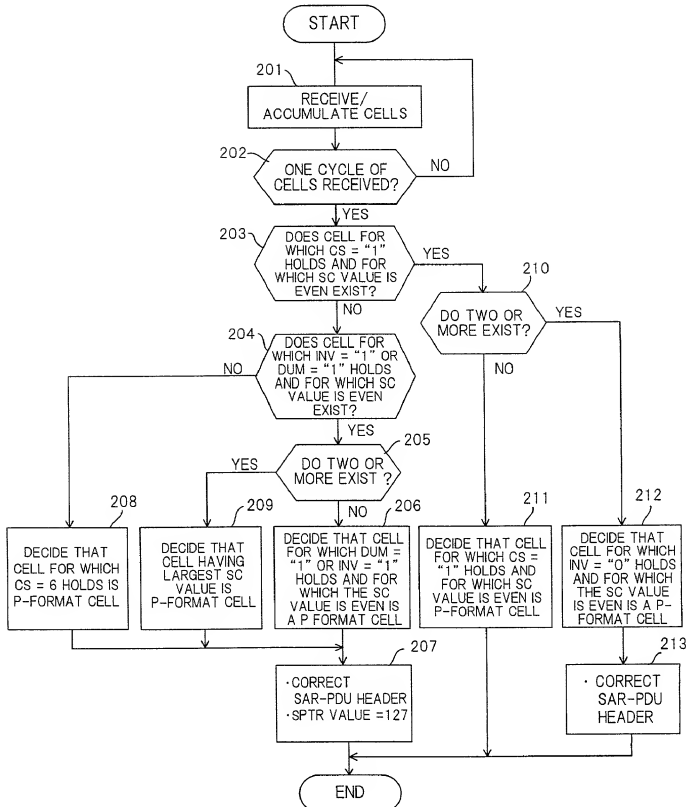


FIG. 27

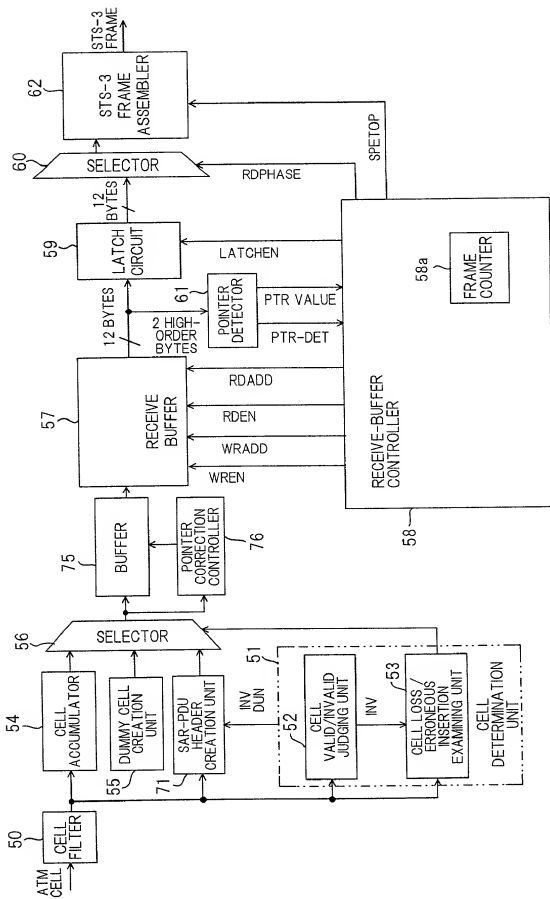


FIG. 28

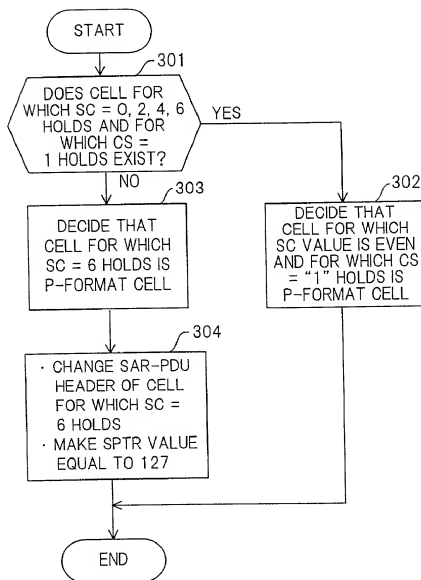


FIG. 29

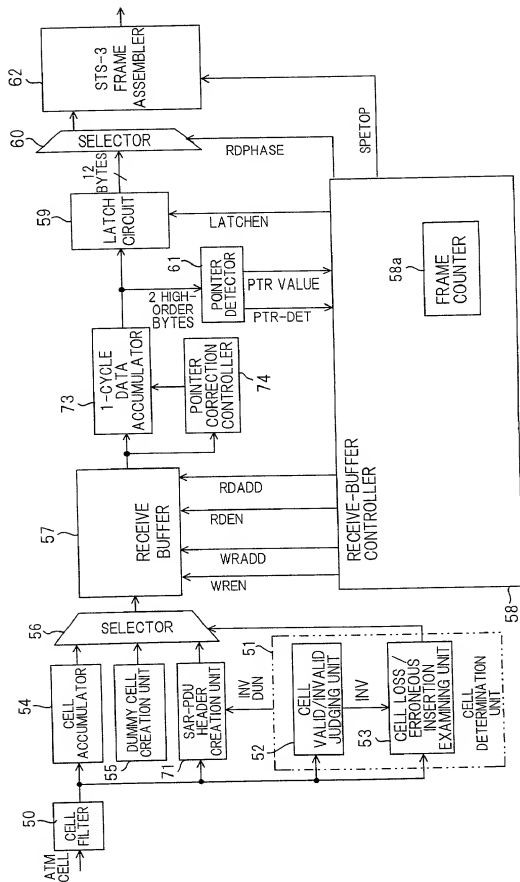


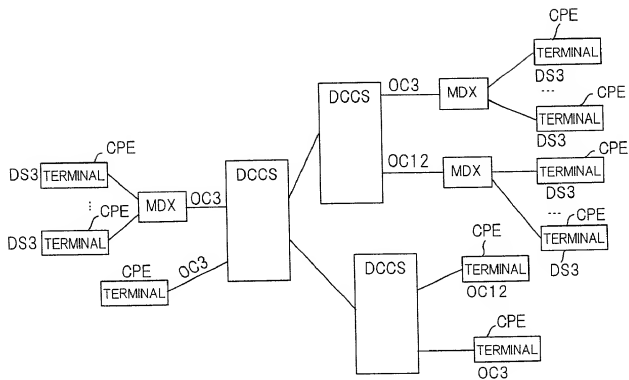
FIG. 30 PRIOR ART

FIG. 31 PRIOR ART